Certified Extract Copy of Register

1. Corporate name

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.

1. Head office

1006, Oaza Kadoma, Kadoma-shi, Osaka

- 1. An article of registration as of April 4, 2001 ··
 - (1) MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. has merged MATSUSHITA ELECTRONICS CORPORATION, 1-1, Saiwai-cho, Takatsuki-shi, Osaka.

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The above description is a certified extract copy of the register.

May 1, 2001

Moriguchi branch office of Osaka Legal Affairs Bureau Greffier MIYAMOTO, Hisatake [seal]

登 記 簿 抄 本

- 1. 商 号 松下電器產業株式会社
- 1.本 店 大阪府門真市大字門真1006番地
- 1. 平成13年4月4日 登記事項 大阪府高槻市幸町1番1号 松下電子工業株式会社を合併

以下余白

上記は登記簿の抄本である。

平成13年5月1日 大阪法務局守口出張所 空記官 宫 本 尚 武

List of the Patents in the Name of MEC.

2	No Application No	Filing Date	Patent No.	Date of Grant	Title
	The second second				
_	09/147462	12/30/1998 6,118,688	6,118,688	09/12/2000	FERROELECTRIC MEMORY DEVICE AND METHOD OF DRIVING THE SAME
					FERROELECTRIC MEMORY SYSTEM AND METHOD OF DRIVING THE SAME ** U.S.
2	09/147598	01/29/1999 6,157,563	6,157,563	12/05/2000	NATIONAL PHASE OF PCT/JP98/02883 FILED 06/26/98 **
					POLISHING PAD FOR SEMICONDUCTOR WAFER AND METHOD FOR POLISHING
m	09/285647	04/05/1999 6,099,390	068'660'9	08/08/2000	SEMICONDUCTOR WAFER
	01/25/00	6 180 472	l	1002/02/10	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
<u>-</u>	03/301213	0//2//222	1	71 201 500 4	
	09/389024	09/02/1999 6,117,700	6,117,700	09/12/2000	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING GROUP III NITRIDE

List of the Applications in the Name of MEC

No. 1 1 2 2 2 3 3 3 4 4 4 4 4 4 4 4 4 4 4 4 4 4	Application No. 09/155420 09/557573 09/341918 09/341918 09/166904 09/166904 09/165994 09/159594 09/15232 09/162232 09/162232	Filing Date 09/29/1998 04/21/2000 07/21/1999 10/06/1998 01/05/2000 09/24/1998 09/25/1998 09/25/1998 01/29/1999 12/16/1999	
13	09/327468	06/08/1999	CHARGE BUILDUP PHENOMENON PATTERN FORMING METHOD METHOD OF MANUFACTURING ELECTRONIC DEVICE
15 16	09/321713	05/28/1999	SEMICONDUCTOR DEVICE AND METHOD AND APPARATUS FOR FABRICATING THE SAME WHILE MINIMIZING OPERATING FAILURES AND OPTIMIZING YIELD METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
17	09/289975	04/13/1999	AMPLIFYING SOLID-STATE IMAGING DEVICE, METHOD FOR DRIVING THE SAME, AND PHYSICAL QUANTITY DISTRIBUTION SENSING SEMICONDUCTOR DEVICE SEMICONDUCTOR DEVICE SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
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Page 1 of 6

List of the Applications in the Name of MEC ...

No.	Application No.	Filing Date	Title
19	09/249844	02/16/1999	METHOD OF FABRICATING INTERCONNECTS UTILIZING FLUORINE DOPED INSULATORS AND BARRIER
20	09/289960	04/13/1999	LAYERS METHOD FOR GROWING NITRIDE COMPOUND SEMICONDUCTOR
21	09/333050	06/15/1999	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
22	09/321581	05/28/1999	SOLID STATE IMAGE DEVICE AND METHOD DRIVING THE SAME
23	09/317613	05/25/1999	CAPACITOR AND METHOD FOR FABRICATING THE SAME
24	09/317604	05/25/1999	METHOD FOR GROWING NITRIDE SEMICONDUCTOR CRYSTALS, NITRIDE SEMICONDUCTOR DEVICE, AND METHOD FOR FABRICATING THE SAME
25	09/366734	08/04/1999	CHARGED PARTICLE LITHOGRAPHY METHOD AND SYSTEM
26	09/292013	04/15/1999	SEMICONDUCTOR LASER DEVICE
27	09/417096	10/13/1999	PRODUCTION CONTROL SYSTEM AND METHOD
28	09/333048	06/15/1999	CMOS INVERTER AND STANDARD CELL USING THE SAME
29	09/338542	06/23/1999	SEMICONDUCTOR MEMORY
30	09/383418	08/26/1999	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
31	09/345495	07/01/1999	SEMICONDUCTOR DEVICE AND METHOD FOR PROVIDING A CONTACT HOLE FORMED IN AN INSULATING FILM
32	09/327467	06/08/1999	
33	09/333049	06/15/1999	FERROELECTRIC MEMORY DEVICE
34	09/448438	11/24/1999	SEMICONDUCTOR STORAGE DEVICE AND METHOD OF MANUFACTURING THE SAME
35	09/383417	08/26/1999	APPARATUS AND METHOD FOR PLASMA ETCHING
36	09/419879	10/15/1999	TERMINAL LAND FRAME AND METHOD FOR MANUFACTURING THE SAME

List of the Applications in the Name of MEC III

No.	Application No.	Filing Date	Title
37	09/432125	11/02/1999	AMPLIFYING SOLID-STATE IMAGING DEVICE, AND METHOD FOR DRIVING THE SAME
38	09/432216	11/03/1999	LEADFRAME FOR USE IN MANUFACTURING A RESIN-MOLDED SEMICONDUCTOR DEVICE
39	09/420455	10/18/1999	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
40	09/417097	10/13/1999	AMPLIFYING SOLID-STATE IMAGING DEVICE, AND METHOD FOR DRIVING THE SAME
41	09/392584	09/09/1999	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
42	09/438584	11/12/1999	AMPLIÈYING SOLID-STATE IMAGING DEVICE, AND METHOD FOR DRIVING THE SAME
43	09/447573	11/23/1999	APPARATUS AND METHOD FOR FEEDING SLURRY
44	09/731011	12/07/2000	APPARATUS AND METHOD FOR FEEDING SLURRY
45	09/479847	01/10/2000	COMPOSITE LIGHT-EMITTING DEVICE, SEMICONDUCTOR LIGHT-EMITTING UNIT AND METHOD FOR FABRICATING THE UNIT
46	09/480942	01/11/2000	BIPOLAR TRANSISTOR AND METHOD OF FABRICATING THE SAME
47	09/450512	11/30/1999	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
48	09/570545	05/12/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
49	09/456381	12/08/1999	METHOD OF MEASURING TEMPERATURE, METHOD OF TAKING SAMPLES FOR TEMPERATURE MEASUREMENT AND METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
20	09/551880	04/18/2000	LITHOGRAPHY PATTERN DATA GENERATION METHOD, LITHOGRAPHY PATTERN FABRICATION METHOD AND CHARGED PARTICLE LITHOGRAPHY SYSTEM
51	09/560299	04/27/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME
22	09/515673	02/29/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
53	09/511371	02/23/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME
54	09/576971	05/24/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

List of the Applications in the Name of MEC.

No.	Application No.	Filing Date	Title
55	09/592266	06/13/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME
56	09/639157		METHOD OF MAKING FERROELECTRIC THIN FILM, FERROELECTRIC CAPICITOR, FERROELECTRIC MEMORY AND METHOD FOR FABRICATING FERROELECTRIC MEMORY
57	09/613631	07/11/2000	ECC CIRCUIT-CONTAINING SEMICONDUCTOR MEMORY DEVICE AND MEMOD OF 11.5 11.10 11.11.
58	09/552622	04/19/2000	METHOD AND APPARATUS FOR REJUVENATING POLISHING SLURRY
59	09/526174	03/15/2000	METHOD OF CLEANING ELECTRONIC DEVICE AND METHOD OF FABRICATING THE SAME
09	09/551541	04/18/2000	METHOD OF FABRICATING SEMICONDUCTOR DEVICE
61	09/551542	04/18/2000	METHOD OF FABRICATING SEMICONDUCTOR DEVICE
29	09/570391	05/12/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME
63	09/679617	10/05/2000	METHOD OF FORMING ELECTRODE STRUCTURE AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE
64	09/680053	10/05/2000	METHOD OF FORMING ELECTRODE STRUCTURE AND METHOD OF FABRICATING SEMICONDUCTOR
65	09/633234	08/04/2000	SEMICONDUCTOR LIGHT-EMITTING UNIT, OPTICAL HEAD APPARATUS AND OPTICAL DISK SYSTEM
99	09/615744	07/13/2000	PHOTOMASK, RESIST PATTERN FORMATION METHOD, METHOD OF DETERMINING ALIGNMENT
29	09/680978	10/06/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME
89	09/661369	09/13/2000	METHOD OF FABRICATING SEMICONDUCTOR DEVICE
69	09/654070	09/01/2000	LEADFRAME AND METHOD FOR MANUFACTURING RESIN-MOLDED SEMICONDUCTOR DEVICE
2	09/639160	08/16/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
77	09/688197	10/16/2000	SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME
72	09/664555	09/18/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

List of the Applications in the Name of MEC

N ON	Annlication No.	Filing Date	Title
			STATE OF TARBUTATING CEMICONDICTOR DEVICE
73	09/662359	09/15/2000	METHOD FOR FABRICALING SEMICONDOCTOR DEVICE.
74	09/662358	09/15/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
75	09/649573	08/29/2000	METHOD FOR DETERMINING A PRECEDING WAFER, METHOD FOR DETERMINING A MEASURING WAFERS WAFER, AND METHOD FOR ADJUSTING THE NUMBER OF WAFERS
76	09/640519	08/17/2000	$\tilde{}$
7.7	09/688196	10/16/2000	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
78	0890£9/60	08/01/2000	PLASMA PROCESSING METHOD
79	09/666156	09/19/2000	HIGH-VOLTAGE MOS TRANSISTOR AND METHOD FOR FABRICATING THE SAME
80	09/729202	12/05/2000	METHOD FOR FABRICATING AN ELECTRONIC DEVICE
81	09/672860	09/29/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
82	09/726654	12/01/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
83	09/662004	09/14/2000	METHOD OF FORMING INSULATING FILM AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE
84	09/680054	10/05/2000	SEMICONDUCTOR DEVICE, SEMICONDUCTOR SUBSTRATE, AND MANUFACTURE METHOD
85	09/661370	09/13/2000	FERROELECTRIC MEMORY DEVICE
98	09/695381	10/25/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
87	09/707844	11/08/2000	SEMICONDUCTOR DEVICE
88	09/729424	12/05/2000	NITRIDE SEMICONDUCTOR DEVICE
89	09/708084	11/08/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
90	09/708082	11/08/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

List of the Applications in the Name of MEC ...

No.	Application No.	Filing Date	Title
91	98080/60	11/08/2000	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
92	09/708085	11/08/2000	METHOD FOR FABRICATING SEMICONDUCTOR DEVICE
93	09/758287	01/12/2001	METHOD FOR GROWING NITRIDE SEMICONDUCTOR CRYSTALS, NITRIDE SEMICONDUCTOR DEVICE, AND METHOD FOR FAVRICATING THE SAME
94	09/714130	11/17/2000	METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE
95	09/735792	12/14/2000	PRINTED WIRING BOARD, IC CARD MODULE USING THE SAME, AND METHOD FOR PRODUCING IC CARD MODULE
96	09/734176	12/12/2000	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME
97	09/745412	12/26/2000	SEMICONDUCTOR DEVICE AND METHOD FOR THE FABRICATION THEREOF
86	09/785503	02/20/2001	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATION THEREOF
66	09/735793	12/14/2000	SEMICONDUCTOR LIGHT EMITTER AND METHOD FOR FABRICATING THE SAME
100	09/783307	02/15/2001	SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

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This will acknowledge receipt of the following:

Recordation Form Cover Sheet for Merger

2. English Translation of Japanese Certified Extract Copy of Register

3. Japanese Certified Extract Copy of Register

4. List of Application Numbers and Patents Numbers in the Name of MEC

5. Check No. 16803 in the amount of \$4200.00 (Merger recordation)

Merger of Matsushita Electronics Corporation with Matsushita Electric Industrial Co., Ltd.

Docket: 740819-0 EJR/SMH:gjc

** PLEASE DATE STAMP AND RETURN **

United States Patent & Trademark Office

Office of Initial Patent Examination

Application papers not suitable for publication

SN	09986997	Mail Date 11	113/01
	Non-English Specification		
	Specification contains drawing(s) on p	page(s)	or table(s)
	Landscape orientation of text	cification 🗆 Cla	ims Abstract
	Handwritten ☐ Specification ☐	Claims	ract
	More than one column Specifica	ition Claims	☐ Abstract
	Improper line spacing	tion Claims	☐ Abstract
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	Abstract not on separate page(s)		
	Improper paper size Must be either	A4 (21 cm x 29.7 cm	n) or 8-1/2"x 11"
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	☐ Drawing page(s)		☐ Claim(s)
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	☐ Glossy pages	☐ Abstract	
	☐ Non-white background	☐ Claim(s)	
	Drawing objection(s)		•
	☐ Missing lead lines, drawing(s)		
	☐ Line quality is too light, drawing(s)		
	☐ More than 1 drawing and not number	ered correctly	
	☐ Non-English text, drawing(s)		
	☐ Excessive text, drawing(s)		
	☐ Photographs capable of illustration,	drawing(s)	